Substitute for form 1449A/PTO	Complete il Known	
	Application Number	10/815,018
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)	Filing Date	March 26, 2004
	First Named Inventor	Frey, Gregor
	Group Art Unit	2193
	Examiner Name	Vu, Tuan
Sheet 1 of 3	Attorney Docket No: 6570P033	

		U	S PATENT D	OCUMENTS			
Examiner Cite USP Document or Issue Date Name of Patentee or Application Initial No Number MM-DD-YYYY of cited Document		nt	Pages, Columns Lines, Where Relev Passages or Relev Figures Appear	vant /ant			
		FORI	EIGN PATEN	IT DOCUMENTS			
Examiner Initials*	Cite No 1	Foreign Patent Document Country Code/Number/Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of cited Document	Where	s, Columns, Lines, Relevant Passages vant Figures Appear	T²

	01	THER DOCUMENTS NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No 1	Include name of the author (in CAPITAL LETTERS), litle of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
		AFFELDT, REYNALD, et al., "Supporting Objects in Run-Time Bytecode	
		Specialization", REYNALD AFFELDT, et al., "Supporting Objects in Run-Time	
		Bytecode Specialization", Department of Graphics and Computer Science, University	1
		of Tokyo, ASIA-PEPM '02, September 12-17, 2002, ACM, pp. 50-60., (Sept. 12-17,	
		2002), 50-60	ļ
		BURTSCHER, et al., "*Automatic Generation of High-Performance Trace	
		Compressors"", <u>IEEE</u> , (2005), pg. 1-12	ļ
		BURTSCHER, M, ""VPC3: A Fast and Effective Trace-Compression Algorithm"",	
		ACM, (2004), pg. 167-176	
		CHANDER, AJAY, et al., "Mobile Code Security by Java Bytecode Instrumentation",	1
		AJAY CHANDER et al., "Mobile Code Security by Java Bytecode Instrumentation",	
		Proceedings of the DARPA Information Survivability Conference & Exposition	
		DISCEX-II 2001, June 12-14, 2001, Stanford University and University of	
		Pennsylvania, [*Partially suppo, (June 12-14, 2001), 14 pgs.	
		CHANG, L., et al., "A Network Status Monitoring System Using Personal Computer",	
		Chang, L.; Chan, WL.; Chang, J.; Ting, P.: Netrakanti, M., "A Network Status	1
		Monitoring System Using Personal Computer", Global Telecommunications	1
		Conference, 1989, and Exhibition. 'Communications Technology for the 1990s and	
		Beyond', GLOBECOM '89., IEEE, (1989), Pgs 201-206	ļ
		COHEN, GEOFF A., et al., "An Architecture for Safe Bytecode Insertion", GEOFF A.	
		COHEN, et al., Software-Practice and Experience, [Version: 2000/03/06 v2.1] "An	
		Architecture for Safe Bytecode Insertion", Department of Computer Science, Duke	1
	·	University (27 pgs.), (03/06/2000), 27 pgs.	
		COHEN, GEOFF A., et al., "Automatic Program Tranformation with JOIE", Paper,	
		Departmen of Computer Science, Duke University, 12 pages.,	
		DAHM, MARKUS, "Welcome to the Byte Code Engineering Library 4.4.1",	
		http://bcel.sourceforge.net/main.html, last updated 4/12/02, 2 pages, printed	1
		September 28, 2006.,	<del> </del>
		DAVIES, JONATHAN, et al., "An Aspect Oriented Performance Analysis	
	ł	Environment", JONATHAN DAVIES, et al., Proceedings of the 2nd international	
		conference on "An Aspect Oriented Performance Analysis Environment", 10 pgs.,	
	<u> </u>	2003, Boston, Massachusetts March 17-21, 2003, (Mar. 17-21, 2003), 10 pgs.	<u> </u>

**EXAMINER** 

DATE CONSIDERED

Substitute for form 1449A/PTO	Complete il Known	
	Application Number	10/815,018
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)	Filing Date	March 26, 2004
	First Named Inventor	Frey, Gregor
	Group Art Unit	2193
	Examiner Name	Vu, Tuan
Sheet 2 of 3	Attorney Docket No: 6570P033	

	TO	THER DOCUMENTS NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No 1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
		GAGNON, ETIENNE, et al., "Effective Inline-Threaded Interpretation of Java	
		Bytecode Using Preparation Sequences", ETIENNE GAGNON, et al., "Effective	
		Inline-Threaded Interpretation of Java Bytecode Using Preparation Sequences", Sable	
		Research Group, Universite du Quebec a Montreal and McGill University, Montreal.	
		Canida, January 23 2003 (15 pgs.)., (Jan. 2003), 15 pgs.	ļ
		GILL, PETER W., "Probing for a Continued Validation Prototype", PETER W. GILL,	
		"Probing for a Continued Validation Prototype", a Thesis Submitted to the Faculty of	
		the Worcester Polytechnic Institute, May 2001, (111 pages), (May 2001), 111 pages.	ļ
		GOLDBERG, ALLEN, et al., "Instrumentation of Java Bytecode for Runtime	
		Analysis", ALLEN GOLDBERG, et al., "Instrumentation of Java Bytecode for Runtime	
		Analysis", Fifth ECOOP Workshop on Formal Techniques for Java-like Programs, July	
		21, 2003, Kestrel Technology, NASA Ames Research Center, Moffett Field, California	İ
		USA, (9 pgs.)., (July 21, 2003), 9 pgs.	
		HAMPTON, KIP,"High-Performance XML Parsing with SAX", Published on	
		XML.com by 14 February 2001.,	
		KAPLAN, et al., ""Flexible Reference Trace Reduction for VM Simulations"", ACM.	
		(2003), pg. 1-38	<u> </u>
		KELLER, RALPH, et al., "Supporting the Integration and Evolution of Components	
		Through Binary Component Adaptation", www.cs.ucsb.edu/oocsb, September 9,	
		1997, Technical Report TRCS97-15, 12 pages.,	<u> </u>
		LEE, HAN B., "BIT: Bytecode Instrumenting Tool", University of Colorado,	
		Department of Computer Science 1997, 51 pages.,	
		LEE, HAN B., et al., "BIT: A Tool for Instrumenting Java Bytecodes", HAN BOK LEE,	
		et al., "BIT: A Tool for Instrumenting Java Bytecodes", originally published in the	
		Proceedings of the USENIX Symposium on Internet Technologies and Systems,	
		Monterey, California, December 1997, www.usenix.org/ (11 pgs.) (December 1997),	
		11 pgs.	<u> </u>
		LI, WEN, et al., "Collaboration Transparency in the DISCIPLE Framework", WEN LI,	
		et al., "Collaboration Transparency in the DISCIPLE Framework", CAIP Center,	1
		Rutgers - The State University of New Jersey, Piscataway, NJ, USA, Proceeding of	
1		the ACM International Conference on Supporting Gruop Work (Group '99) November	
		14-17, 1999, (Nov. 14-17, 1999), 10 pgs.	
		MACRIDES, NATHAN, "SANS Security Essentials (GSEC) Practical Assignment	
		Version 1.4", NATHAN MACRIDES, Security Techniques for Mobile Code "SANS	
		Security Essentials (GSEC) Practical Assignment Version 1.4", July 11, 2002, (11	
		pgs.), (7/11/02), 11 pgs.	ļ
		MCLAUGHLIN, BRETT, "Tip: Set up a SAX Parser", Published by International	
		Business Machines Corporation on 2 July 2003.	
		MCNAMEE, DYLAN, et al., "Specialization Tools and Techniques for Systematic	
		Optimization of System Software", DYLAN McNAMEE, et al., "Specialization Tools	<u></u>

EXAMINER DATE CONSIDERED

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)	Complete if Known		
	Application Number	10/815,018	
	Filing Date	March 26, 2004	
	First Named Inventor	Frey, Gregor	
	Group Art Unit	2193	
	Examiner Name	Vu, Tuan	
Sheet 3 of 3	Attorney Docket No: 6	5570P033	

	01	THER DOCUMENTS NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No 1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		and Techniques for Systematic Optimization of System Software", Oregon Graduate	
		Institute of Science & Technology, and University of Rennes/IRISA, ACM	ł
		Transactions on Computer Systems, 2001 (30 pgs.), (2001), 30 pgs.	
		NIKOLOV, NIKOLAI, "Classfile Conversion Into An Organization of Objects, and	
		Modification Thereof, To Effect Bytecode Modification", NIKOLOV, NIKOLAI,	
		"Classfile Conversion Into An Organization of Objects, and Modification Thereof, To	1
		Effect Bytecode Modification", Serial number 10/750,396, Office Action mailed	ŀ
		September 5, 2006. Copy of the Offie Action, claims as they stood in the app,	
		NIKOLOV, NIKOLAI, "Execution of Modified Byte Code for Debugging, Testing	
		And/Or Monitoring of Object Oriented Software", NIKOLAI NIKOLOV, "Execution of	
		Modified Byte Code for Debugging, Testing And/Or Monitoring of Object Oriented	
		Software", Serial number10/749,617, filed on December 30, 2003, Ofice Action mailed	
		August 23, 2006. Copy of the Office Action, claims as they sto,	
		PLOESH, REINHOLD, "Evaluation of Assertion Support for the Java Programming	
		Language", REINHOLD PLOESH, Johannes Kepler University Linz, Austria,	
		"Evaluation of Assertion Support for the Java Programming Language", JOT: Journal	
		of Object Technology, Vol. 1, No. 3, Special Issue: TOOLS USA 2002 Proceedings,	
		pp. 5-17, http://www.jot.fm/issues/, (2002), 5-17	
·		RUDYS, ALGIS, et al., "Enforcing Java Run-Time Properties Using Bytecode	
		Rewriting", ALGIS RUDYS, et al., "Enforcing Java Run-Time Properties Using	
		Bytecode Rewriting", International Symposium on Software Security (Tokyo, Japan),	
		November 2002, Rice University, Houston, TX 77005, USA (16 pgs.)., (November	
		2002), 16 pgs.	
		SNYDER, ALAN, "The Essence of Objects: Concepts and Terms", <u>IEEE Software</u> , January 1993, pp. 31-42, Sunsoft, Mountain View,	
		STEPHENSON, BEN, et al., "Characterization and Optimization of Java	1
		Applications", BEN STEPHENSON, et al., "Characterization and Optimization of Java	
		Applications" Department of Computer Science, Abstract in Western Research Forum	
		Program & Abstracts, page 20, 2003., (2003), Page 20	
		WELCH, IAN, et al., "Kava - A Reflective Java Based on Bytecode Rewriting", IAN	<b>†</b>
		WELCH, et. al., "Kava - A Reflective Java Based on Bytecode Rewriting" SpringerLink	
		- Verlag Berling Heidelberg 2000, Chapter, Lectures Notes in Computer Science, W.	
		Cazzola, et al. Editors, Reflection and Software Engineering, LNCS, pages 155-167.	
	•	(2000), 155-167	
		WU, et al., ""From Trace Generation to Visualization: A Performance Framework for	
		Distributed Parallel Systems", <u>IEEE</u> , (2000), pp. 1-18	1

EXAMINER DATE CONSIDERED